## N THE SNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of	)
	)
ICHISE et al.	) Art Unit 2812
	)
Application Number: 10/800,694	) Examiner H. Jey Tsai
	)
Filed: March 16, 2004	)
	)
For: Semiconductor Integrated Circuit Device	)
AND THE METHOD OF PRODUCING THE SAME	)
	)
Attornev Docket No. HITA.0529	)

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

## **COVER LETTER**

Sir:

[x] The fee for submission of claims is calculated as shown below:

For	TOTAL WITH NEW CLAIMS ADDED	TOTAL CURRENTLY ON FILE	CLAIMS PAID	RATE	CALCULATION
Total Claims	17	17	(Over 20)	x \$18	0
Independent Claims	3	3	(Over 3)	x \$86	0
MULTIPLE DEPENDENT CLAIM(S)				+ \$290	0
REDUCTION FOR FILING BY SMALL ENTITY (note 37 C.F.R. §§ 1.9, 1.27, 1.28).  X ½  IF APPLICABLE, VERIFIED STATEMENT MUST BE ATTACHED			x ½		
TOTAL		L	0		

In addition, the below-identified communications are submitted in the above-captioned application or proceeding:

[x] Response/Amendment	[ ] Petition for Extension of Time ( month)
(with claim amendments)	[ ] Information Disclosure Statement
[ ] Preliminary Amendment	[ ] Letter to Draftsperson
[ ] Substitute Specification	[ ] sheets of drawings
[ ] Request for Continued Examination	[ ] Petition under

[ ]		ount Number in the amount of to cover the fees for licate copy of this paper is enclosed.			
[ ]	A check in the amount of \$	for the fee is enclosed.			
[x ]	The Commissioner is hereby authorized to charge any additional fees associated with this communication, or credit any overpayment to <b>Deposit Account Number 08-1480</b> .				
		Respectfully submitted,			
		Stanley P. Fisher			
		Registration Number 24,344			
		AllAlla			
		Juan Carlos A. Marquez			
		Registration No. 34,072			

REED SMITH LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 September 1, 2004

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## **RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

This is in response to the Office Action mailed on August 25, 2004, the period of response to which is set to expire on September 25, 2004. Applicants hereby elect the continuing prosecution of Group II as recited in claims 1 - 12, directed to a semiconductor device, without traverse.